Seat	Total No. of Pages: 10
No.	

## P.G. Entrance Examination, May - 2023 M.Sc. ELECTRONICS

Sub. Code: 58298

•			sday, 09 - 05 - 2023 o 05.00 p.m.		Total Marks: 100
Inst	ructions	: 1)	All questions are co	ompulsory.	
		2)	Each question carr	ries 1 mark.	
		3)	Answers should be darkening the appr		the given OMR answer sheet by
		4)	Follow the instruct	tions given on	OMR sheet.
		5)	Rough work shall be paper.	oe done on the	sheet provided at the end of question
1)	The I	C 7490	is a		
	A) I	O flip Fl	lop	B)	T Flip Flop
	C) I	Decade	Counter	D)	Both A & B
2)	A mu	ltiplexer	is also called as a		
	A) (	Coder		B)	Parallel adder
	C) I	Data sele	ector	D)	NOR gate
3)	Binar	y ripple	counter with 4 fli	p-Flops cou	nts states.
	A) 1	16		B)	8
	C) 4	1		D)	2
<b>4</b> )	8085	micropr	ocessor has	_ bit data b	us and bit address bus.
	A) 8	3, 64		B)	8, 16
		3, 32		D)	16, 32
<b>5</b> )	Induc	tive read	ctance is given by	X, =	
		$2\pi f L$	-	_	$4\pi f L$
	C) 2	2fL		D)	$2\pi f$

<b>6</b> )	If reverse bias is applied to the PN junction diode, then its barrier widt						
	A)	increases	B)	decreases			
	C)	remains the same	D)	none of these			
<b>7</b> )		Diode is used for voltage i	egula	ation.			
	A)	LED	B)	varactor			
	C)	zener	D)	avalanche			
8)	Whi	Which of the following is universal gate?					
	A)	AND	B)	NAND			
	C)	OR	D)	XOR			
9)	The 8421 code is code						
	A)	Binary	B)	BCD			
	C)	Gray	D)	Unweighted			
10)	The	half adder adds bits.					
	A)	2	B)	3			
	C)	4	D)	8			
11)	The	boolean expression A+AB =					
	A)	AB	B)	A+B			
	C)	A	D)	В			
12)	The	ripple factor of a bridge rectifier is					
- <b>-</b> )		0.482		0.812			
	C)	1.11	D)	1.21			

13)	In full wave rectification, if the input frequency is 50 Hz, then output frequency is Hz.					
	A)	25	B)	50		
	C)	100	D)	200		
14)		amplifier is also called as em	itter fo	llower.		
	A)	common emitter	B)	common base		
	C)	common collector	D)	all of the above		
15)	Blo	cking capacitor blocks	signal.			
	A)	a.c.	B)	d.c.		
	C)	both a.c. and d.c.	D)	none of these		
<b>16</b> )		type of amplifier, transis	stor cor	nducts for complete cycle of the		
	A)	class-A	B)	class-B		
	C)	class-C	D)	class AB		
<b>17</b> )	For	ward biased diode offers	_ resista	ance.		
	A)	zero	B)	very low		
	C)	very high	D)	infinite		
18)	Tra	nsistor amplifier which gives curr	ent gai	n ≈ 1 is		
	A)	CE	B)	СВ		
	C)	CC	D)	none of these		
<b>19</b> )	The	flip flop has two outputs which	are	of each other.		
	A)	Complements	B)	Same		
	C)	Opposite	D)	Both A & C		

20)		ase of Kirchhoff's current law, cursidered	rents	meeting the junction point are		
	A)	positive	B)	negative		
	C)	zero	D)	none of these		
21)		region of the transistor is ligh	ntly d	oped.		
	A)	Emitter	B)	Base		
	C)	Collector	D)	None of these		
22)	Bas	e of the hexadecimal system is	•			
	A)	2	B)	8		
	C)	10	D)	16		
23)	Shif	t registers are used for				
	A)	shifting	B)	rotating		
	C)	adding	D)	both A and B		
24)	Pow	ver in electrical circuit is given by				
		IV		$I^2 R_{_{\rm L}}$		
	C)	$V^2 R_L$	D)	All of these		
25)	Thevenin's network gives an equivalent network in form					
	A)	shunt	B)	series		
	C)	ladder	D)	T		
26)	max	is the source resistance and $R_L$ is imum power transfer theorem, max				
	A)	$R_s > R_L$	B)	$R_s < R_L$		
	C)	$R_s = R_L$	D)	$R_s = R_L = 0$		

27)		condition under which fourier s	series	can be expanded is known as			
		Convergence	B)	Fourier			
	C)	Periodic	D)	Dirichlet			
28)	Lap	lace transform of unit step function	n is _				
	A)	1 / S	B)	$1 / S^2$			
	C)	$1/S + \alpha$	D)	$1/S-\alpha$			
29)		configure all ports of 8255 as outpuld be	out po	rts in mode 0, the control word			
	A)	00 H	B)	1B H			
	C)	80 H	D)	9B H			
30)		is a non-maskable interrup	t				
		RST 5.5		RST 6.5			
	C)	RST 7.5	D)	TRAP			
31)	Asta	able multivibrator has sta	ble sta	ates.			
	A)	One	B)	Two			
	C)	No	D)	None of these			
32)	In I	C 555 pin no. 2 stands for	•				
	A)	Threshold	B)	Reset			
	C)	Discharge	D)	Trigger			
33)		To interface EPROM IC 2764 with 8085, the minimum address lines are required.					
	A)	11	B)	12			
	C)	13	D)	14			

34)		is a memory pointer register.		
	A)	ACC	B)	Flag
	C)	PC	D)	IR
35)	LXI	H, 6000H is instruction.		
	A)	One byte	B)	Two byte
	C)	Three byte	D)	None of these
36)	The	Voltage to Current Converter is als	o cal	led as a
	A)	Current - series negative feedback a	ampli	fier
	B)	Current - series positive feedback a	mpli	fier
	C)	Voltage - series positive feedback a	mpli	fier
	D)	Voltage - series negative feedback a	ampli	fier
<b>37</b> )		output voltage of a phase detector rred to as the voltage.	in P	LL is a dc voltage and is often
	A)	actual	B)	error
	C)	measured	D)	both A) & B)
38)	The	output impedance of a voltage regu	ulatoı	· is
	A)	Very small		
	B)	Very large		
	C)	Equal to the load voltage divided b	y the	load current
	D)	Equal to the input voltage divided	by th	e output current
<b>39</b> )	The	instruction MVI C, 25 H has	ad	ldressing mode.
	A)	Direct	B)	Register
	C)	Indirect	D)	Immediate

<b>40</b> )	In h - Parameter representation, the independent variables are			
	A)	$I_1$ and $V_2$	B)	$I_2$ and $V_1$
	C)	$V_1$ and $V_2$	D)	$\boldsymbol{I_1}$ and $\boldsymbol{I_2}$
44\	3.6			
41)	Mod	dulation is done at		
	A)	Transmitter	B)	Receiver
	C)	Both receiver & transmitter	D)	None of these
42)	805	1 has bytes of internal me	emor	v
)		128	B)	
	ŕ	64K	D)	
43)		mode is called as auto reloa	ad m	ode
	A)	Mode 0	B)	Mode 1
	C)	Mode 2	D)	Mode 3
44)	Inte	rrupt priority can be changed using		register.
/	A)		B)	IP
	C)	TCON	D)	TMOD
45				
45)	An 1	integrated circuit offers		
	A)	Improved performance	B)	High speed
	C)	Low powers consumption	D)	All the (A), (B) & (C)
<b>46</b> )	The	main advantages of differential amp	olifie	r is that it rejects
•	A)	Common - mode signal	B)	Distortion
	C)	Differential mode signal	D)	Noise

<b>47</b> )	The	ideal op-amp has output	t resi	stance.
	A)	75 Ω	B)	$0 \Omega$
	C)	$2M \Omega$	D)	Infinite $\Omega$
48)	Sup	ply voltage rejection ratio(SVRR) o	of op-	-amp is the measure of
,		voltage gain	-	I/P impedence
	C)	noise	D)	o/p impedence
<b>49</b> )	For	an ideal Op-Amp slew rate is	•	
	A)	Zero	B)	Infinite
	C)	0.5 V/s	D)	0.5 u V/s
<b>50</b> )	De-1	modulation is done at		
ŕ		Transmitter	B)	Receiver
	C)	Both receiver & transmitter	D)	None of these
<b>51</b> )	In A	.m, when Em = Ec then this conditi	on is	known as
	A)	Under modulation	B)	Over modulation
	C)	Ideal condition	D)	None of these
52)	In ra	adio wave prorogation, the ground v	vave	s are also called as
	A)	Space waves	B)	Ionosphere wave
	C)	Sky waves	D)	Surface wave
53)	In T	V sound is modulated		
	A)	Amplitude	B)	Frequency
	C)	Angle	D)	None of these

54)	Low	vest existing layer in an ionosphere	is	·			
	A)	E-Layer	B)	F1-Layer			
	C)	D-Layer	D)	F2-Layer			
55)	In a	ny communication system the S/N	ratio	should be			
	A)	as low as possible	B)	as hgih as possible			
	C)	infinite	D)	constant			
<b>56</b> )	Con	nmunication through walky-talky is	a	system.			
	A)	simplex	B)	half duplex			
	C)	full duplex	D)	none of these			
57)	The crystal frequency extensively used for 8051 is 11.0592 MHz especially						
	A)	to match baud rate while usir communication	ng 80	051 and IBM PCs for serial			
	B)	B) due to these crystals are available at low cost					
	C)	it is suitable for delay generation using timers					
	D)	it is suitable for Interrupt handling					
<b>58</b> )	For	the baud rate of 4800, the value of	TH1	is			
	A)	-3	B)	-6			
	C)	-12	D)	-24			
<b>59</b> )		8051 serial communication h indicating that the last character w		_			
	A)	TI	B)	RI			
	C)	REN	D)	All above			

<b>60</b> )	The	meaning of sbitmybit=P2^7 is		
	A)	P2s EXORed with P2.7	B)	7 bits are XORed with P2
	C)	P2.7 bit is labeled as mybit	D)	None of these
61)		nsigned character x = 0x 0F and A x x Contents of A becomes		55 After executing instruction
	A)	0x F0	B)	0x FF
	C)	0x 05	D)	0x 55
<b>62</b> )		nsigned character x=0x 0F, After exe A becomes	ecutin	g instruction A=x >>2 Contents
	A)	0x F0	B)	0x 00
	C)	0x 03	D)	0x 55
<b>63</b> )		nicrocontroller and LCD interface _ rocontroller is sending data.		_ line will instruct the LCD that
	A)	RW	B)	RS
	C)	EN	D)	D0
64)		stepper motor takes 45 steps to mal	ke on	e complete revolution then step
	A)		B)	12
	C)	8	D)	90
<b>65</b> )		power transistor has a b sistor due to the presence of the drift		_
	A)	higher	B)	moderate
	C)	lower	D)	none of these
66)	The	reverse recovery current in power	diode	e depends upon
-	A)	storage charge	B)	forward current
	C)	temperature	D)	PIV

<b>67</b> )	The	IGBT has the following terminals		·
	A)	emitter, base, gate	B)	emitter, base, collector
	C)	emitter, base, drift	D)	emitter, collector, gate
<b>68</b> )		is a unidirectional device with	h thre	ee terminals
	A)	Diac	B)	ВЈТ
	C)	SCR	D)	Triac
<b>69</b> )	The	voltage measured between any two	phas	ses is called as voltage.
	A)	line	B)	phase
	C)	both A & B	D)	none of these
<b>70</b> )	SM	PS are superior to linear power sup	plies	in respect of
	A)	efficiency and regulation	B)	size and efficiency
	C)	regulation and noise	D)	noise and cost
<b>71</b> )		problem of dc input current can be ja single phase full wave controller v	•	•
	A)	constant	B)	unidirectional
	C)	bidirectional	D)	none of these
72)	The	Power MOSPET is a carri	er de	vice
	A)	majority	B)	minority
	C)	both A & B	D)	none of these
<b>73</b> )		wo position control two different se e set points is called	t poi	nt are used and the gap between
	A)	hysteresis	B)	neutral zone
	C)	inter set point gap	D)	error

74) controller cannot minimize the sudden error.			udden error.	
	A)	Multi-position control	B)	Proportional control
	C)	Proportional Integral control	D)	PID control
<b>75</b> )		communication protocol use	ed in	automation systems
	A)	RS232	B)	IEEE 801.1
	C)	RS-485	D)	RS-486
<b>76</b> )		control loop are used in lar	ge in	dustrial plants.
	A)	Feed forward	B)	Feedback
	C)	Local	D)	Direct
77)		switches are not accessible	for t	the operator.
	A)	SPDT	B)	Limit
	C)	Push button	D)	Rotary
<b>78</b> )	PLC	Cs can be in plant to change	e the	sequence of operation.
	A)	only programmed		
	B)	programmed and reprogrammed		
	C)	only reprogrammed		
	D)	to give a set point table		
<b>79</b> )	Which of the following cannot be an input that is given to the PL			
	A)	Manual Switches	B)	Relays
	C)	Senors	D)	None of these
80)	Whi	ich one of the following communica	tion s	system is digital?
	A)	FM	B)	AM
	C)	PCM	D)	PAM

81)	RFII	RFID stands for			
	A)	Radio Frequency Infrared Detector			
	B)	Radio Frequency Integrated Data			
	C)	Radio Frequency Intrinsic Data			
	D)	Radio Frequency Identification			
82)	Celli	ular phone is a system.			
o <b>_</b> )	A)	simplex	B)	full duplex	
	ŕ	half duplex	D)	-	
	-,		_ /		
83)	The	he propagation of light through OFC cable is based on the principle of			
	A)	dispersion	B)	refraction	
	C)	reflection	D)	absorption	
84)	Marl	k and space refers respectively to _		·	
	A)	dot and dash	B)	binary '1' and '0'	
	C)	message and interval	D)	on and off	
85)	The	he ringing voltage supplied by the telephone exchange is			
	A)	an internal ac power supply	B)	a battery	
	C)	48 Vdc by the telephone exchange	D)	230 Vac from ac mains	
86)	A K	nown accurate measure of physical	ดมลท	tity is termed as a	
<i>30)</i>	A)	accuracy	B)	standard	
	ŕ	resolution		error	
	$\cup$ $I$	10801UHOH	וע	CHUI	

<b>87</b> )	Alk	aline solution have a pH value		than 7
	A)	greater	B)	less
	C)	equal	D)	none of these
88)	То г	measure the of a rotating s	haft	tachometer is used.
	A)	current	B)	voltage
	C)	speed	D)	power
<b>89</b> )	The	transmit and receive frequencies of	Cell	phone are selected by
	A)	MTSO	B)	handset manufacturer
	C)	the calling subscriber	D)	called subscriber
90)	PIC	18f458 will reset when its pin	•	
	A)	MCLR is low	B)	MCLR is high
	C)	MCLR is open	D)	OSC1 is low
91)	PIC	18f458 is bit controller	r.	
	A)		B)	16
	C)	32	D)	64
92)		6 Mhz external quartz crystal is couck frequency will be Mhz.	nnect	ted to PIC 18f458, then internal
	A)	8	B)	16
	C)	4	D)	32
93)	PIC	18f458 has, 8 bit I/O POR'	TS.	
,	A)		B)	5
	C)	4	D)	2

94)	After execution of an instruction ADDWF f,d,aifd=0, the result will lin register.			l,aifd=0, the result will be stored	
	A)	File	B)	WREG	
	C)	Both A and B	D)	None of the above	
95)		degree of exactness of measurement ded as	nt coi	mpared to the expected value is	
	A)	Accuracy	B)	Resolution	
	C)	Precision	D)	Error	
96)		is not an active transducer.			
	A)	Photodiode	B)	Thermister	
	C)	Phototransistor	D)	None of these	
<b>97</b> ) The LVD		LVDT is a type of tra	T is a type of transducer.		
	A)	Capacitive	B)	Resistive	
		Active	D)	Passive	
98)	The	differential amplifier can be biased	by u	sing	
		emitter bias		current mirror bias	
	C)	constant current bias	D)	all of these	
<b>99</b> )	PIC	18f458 has I/O PINS.			
·	A)	32	B)	34	
	C)		D)	36	
100	)To r	replay the recorded data many times	S	Recorder is used.	
ĺ	(A)	X-T		Magnetic tape	
		X-Y	D)	None of these	

## Rough Work