Shivaji University, Kolhapur
Revised Syllabus Structure
Electronics and Telecommunication Engineering
Scheme of Teaching and Examination
M.E. (ELECTRONICS & TELECOMMUNICATION ENGINEERING)

(To be implemented from 2015-2016)

Semester- I

<table>
<thead>
<tr>
<th>Sr No</th>
<th>Subject</th>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>L</td>
<td>T</td>
</tr>
<tr>
<td>1</td>
<td>Advance Embedded System</td>
<td>3</td>
<td>-</td>
</tr>
<tr>
<td>2</td>
<td>Error control Coding Techniques</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>Advance Wireless Communication</td>
<td>3</td>
<td>-</td>
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<tr>
<td>4</td>
<td>Random Process</td>
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<tr>
<td>5</td>
<td>Elective I</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>6</td>
<td>Seminar- I</td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>Comprehensive viva</td>
<td></td>
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<tr>
<td></td>
<td>TOTAL</td>
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</tr>
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</table>

Semester- II

<table>
<thead>
<tr>
<th>Sr No</th>
<th>Subject</th>
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<tbody>
<tr>
<td></td>
<td></td>
<td>L</td>
<td>T</td>
</tr>
<tr>
<td>1</td>
<td>Advance microwave circuit design</td>
<td>3</td>
<td>-</td>
</tr>
<tr>
<td>2</td>
<td>Adhoc&amp; wireless sensor networks</td>
<td>3</td>
<td>-</td>
</tr>
<tr>
<td>3</td>
<td>computer vision</td>
<td>3</td>
<td>-</td>
</tr>
<tr>
<td>4</td>
<td>Elective II</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>5</td>
<td>Elective III</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>6</td>
<td>Seminar- II*</td>
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<td></td>
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<tr>
<td>7</td>
<td>Comprehensive viva</td>
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</tr>
<tr>
<td></td>
<td>Total</td>
<td>15</td>
<td>2</td>
</tr>
</tbody>
</table>

*-Students should prepare research proposal on any technical topic & Present the seminar.
<table>
<thead>
<tr>
<th>Elective-I</th>
<th>Elective-II</th>
<th>Elective-III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mobile Computing</td>
<td>Cryptography &amp; Network Security</td>
<td>Advanced Operating Systems</td>
</tr>
<tr>
<td>Digital Data Compression</td>
<td>Multi rate system</td>
<td>Optimization techniques</td>
</tr>
<tr>
<td>Internet Traffic engineering</td>
<td>SDR &amp; Cognitive Radio Technology</td>
<td>Industry automation &amp; process control</td>
</tr>
<tr>
<td>Advanced Antenna Theory</td>
<td>DSP architecture &amp; algorithm</td>
<td>Nanotechnology</td>
</tr>
</tbody>
</table>

**Semester- III**

<table>
<thead>
<tr>
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</thead>
<tbody>
<tr>
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<td></td>
<td>L</td>
<td>T</td>
</tr>
<tr>
<td>1</td>
<td>Seminar-III</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>2</td>
<td>Dissertation Phase-I</td>
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<td>-</td>
</tr>
<tr>
<td>Total</td>
<td></td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

1. TW marks in Seminar-III shall be based on the delivery of at least two seminars in semester III, The topic of both seminars shall be related to his/her dissertation topic.

2. TW marks for dissertation phase I shall be based on work carried out by the candidate based on his/her dissertation work in consultation with his/her guide. This work may also include software assignment, field work, industrial training, etc. as decided by guide. The student shall submit monthly progress report to the department. The student shall deliver a presentation at the end of semester III based on the work

3. Practical batch will be comprised of 9 students.

4. P. G. Recognized teacher within university can be appointed as an external examiner.

**Semester -IV**

<table>
<thead>
<tr>
<th>Sr No</th>
<th>Subject</th>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>L</td>
<td>T</td>
</tr>
<tr>
<td>1</td>
<td>Dissertation Phase-II</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Total</td>
<td></td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
1. TW marks for dissertation phase II shall be based on work carried out by the candidate based on his/her dissertation work in consultation with his/her guide. This work may also include software assignment, field work, industrial training, etc. as decided by guide. The student shall submit progress report after every fifteen days to the department. The student shall deliver a presentation at the end of semester IV based on the work.

2. *Dissertation phase II Oral examination shall be based on the work carried out by the candidate in dissertation phase I and phase II.

**Comprehensive viva**

The students have to prepare on all subjects which they have studied in 1st and 2nd semesters.

The viva will be conducted by the External/Internal Examiner jointly and their appointments will be made by university (PG recognized teachers in Shivaji University).

The in-depth knowledge, preparation and subjects understanding will be assessed by the Examiners.
SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- I
w.e.f July 2015
1. Subject: Advanced Embedded Systems

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Lectures : 03 hrs / week</td>
<td>Theory : 100 Marks</td>
</tr>
<tr>
<td>Practical: 02 hrs / week</td>
<td>TW :25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. To Understand architecture of ARM family.
2. To Understand On chip peripherals of ARM controller.
3. To Understand basic concepts of RTOS and µCOS

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Design the ARM based systems.
2. Implement use of ON CHIP peripherals of ARM
3. Implement various scheduling algorithms

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td>ARM9 architecture and programming</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td>ARM9 architecture, Memory organization, Programmers model, instructions and assembly programming.</td>
<td></td>
</tr>
</tbody>
</table>

| II       | ARM caches MPU and MMU: | 8 |
|          | Cache architecture, Cache policy, Coprocessor15 and caches, protected region, Initializing MPUs, caches and write buffer, virtual memory, ARM MMU, page tables, TLB, Coprocessor15 and MMU operation |

| III      | ARM Peripherals and Programming | 8 |
|          | On chip peripherals, GPIO, Interrupts, RTC, Watchdog, UART, I²C, ADC and SPI interfacing and programming using Embedded ‘C’, CAN, LIN, USB, (LPC 29xx series Example 2921/23/25), |

<p>| IV       | Introduction to RTOS | 5 |
|          | RTOS basics, RTOS architecture, share data problem, critical section, shared resources, Task states multitasking, context switching, Kernels, pre-emptive &amp; non-pre-emptive schedulers, mutual exclusion, semaphores, Interrupt Latency, pipes &amp; mails boxes. |</p>
<table>
<thead>
<tr>
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</thead>
<tbody>
<tr>
<td><strong>V</strong></td>
<td><strong>μCOS</strong>&lt;br&gt;Kernel Structure: Tasks, Task State, Task Level Context Switching, Locking and unlocking of scheduler, Idle Task, Statistics Task, Interrupts, Clock Tick, Initialization, Starting the OS, Task Management: Creating/deleting and Suspending/ Resuming Task, Task Stacks and checking, Changing Task’s Priority.</td>
</tr>
<tr>
<td><strong>VI</strong></td>
<td><strong>Time management and event control blocks</strong>&lt;br&gt;Time Management: Delaying/Resuming Task, System Time, Event Control Blocks: Initialization of ECB, Placing/Removing Task from ECB waitlist, Finding Highest Priority Task, List of Free ECB, Task State Management. Communication in μCOS-II.</td>
</tr>
</tbody>
</table>

**Text Books**

1. ARM System Developers Guide, Designing & Optimizing System Software by Andrew sloss, Dominic symes, Chris Wright.- ELSEVIER Publication

**Reference Books**

1. Embedded software primer by David Simon, Person Education.
2. ARM LPC 29xx series data sheet

**List of Experiments (Minimum 8)**

1. Any 2 experiments on Assembly language programming on LPC 29XX series.
2. Any 5 experiments on ARM using Embedded “c” on kiel.
3. Any 3 experiments on RTOS/ μCOS.
SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- I
w.e.f July 2015
2. Subject: Error Control Coding Techniques

Teaching Scheme  Examination Scheme
Lectures : 03 hrs / week  Theory : 100 Marks
Tutorial: 01 hrs / week  TW : 25 Marks

Course Objectives: The course aims to:

1. To Understand basic concept & need of Error Control Coding
2. To Study of various encoding & decoding techniques through block codes
3. To Study of various encoding & decoding techniques through Convolutional Codes.

Course Outcomes: Upon successful completion of this course, the student will be able to:

1. Understand and identify the role of Error Control Coding techniques.
2. Capable to Analyze & design the encoder & decoder of Block Codes.
3. Analyze the concept of encoding & decoding procedures in convolutional codes.

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td><strong>Linear block codes</strong></td>
<td>6</td>
</tr>
<tr>
<td>I</td>
<td>Need, Objective &amp; Approaches of Error Control Coding, Introduction, Structure, Parameters, Generator &amp; Parity Check Matrix, Encoding circuit for (n-k) Linear Block Code, Syndrome &amp; Error detection, Syndrome circuit, Distance Properties, Error detecting &amp; Correction Capabilities, Standard Array &amp; Syndrome decoding for (n,k) linear Block Code, Hamming Codes, Product codes, Repetition code, Hadamard codes (Wash Code), Dual Code, Shortened and Extended linear Codes, Reed Muller (RM) Codes.</td>
<td>6</td>
</tr>
<tr>
<td>II</td>
<td><strong>Cyclic codes</strong></td>
<td>6</td>
</tr>
<tr>
<td>II</td>
<td>Algebraic structure, Polynomial representation of codeword, Generator polynomial, Non-systematic &amp; Systematic Cyclic Codes, Generator &amp; Parity Check Matrices, Structure of Cyclic Encoder &amp; Syndrome Calculator, Encoding of cyclic code using (n-k) &amp; K shift register, Syndrome computation and Error detection, Decoding of Cyclic code, Error-Trapping Decoding, Cyclic Redundancy Check Code, Cyclic Hamming Codes, Golay Code, Shortened Cyclic Codes, Cyclic Product Code, Quasi Cyclic Code.</td>
<td>6</td>
</tr>
</tbody>
</table>
### Bose Chaudhuri Hocquenghem CODE (BCH)
- Groups, Rings & its properties, Fields : Binary Field Arithmetic, Primitive element and primitive polynomial, Primitive BCH Code, Construction of Galois Field GF( 2\textsuperscript{m}), Properties of Galois Field GF( 2\textsuperscript{m}), Minimal & Generator Polynomial for BCH Code.

### Reed-solomon codes & decoding algorithms

### Convolutional codes

### Iteratively decoded codes
- Low Density Parity Check Codes (LDPC) : Introduction, Construction, Tanner Graph, Decoding LDPC Code: Hard & Soft decoding, Vertical Step updating, Horizontal Step Updating, Terminating & Initializing the decoder algorithm.

### Text Books

### Reference Books

**Minimum 8 tutorials based on above syllabus**
SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- I
w.e.f July 2015
3.Subject: Advanced Wireless Communications

Teaching Scheme | Examination Scheme
-----------------|---------------------
Lectures : 03 hrs / week | Theory :100 Marks
Practical: 02 hrs / week | TW :25 Marks

Course Objectives:
The course aims to:
1. To acquire fundamental knowledge of Wireless Communications.
2. To study the different interferences in wireless channels and propagation models.
3. To study capacity of wireless channels and multiple antenna system.
4. To understand the basic concepts of OFDM.

Course Outcomes:
Upon successful completion of this course, the student will be able to:
1. Students will be able to understand the need and applications of Wireless Communication.
2. Students will be able to design propagation model as per the application requirement.
3. Students will be able to implement Multiple Input Multiple Output antenna System.
4. Students will be able to utilize the knowledge of OFDM IN high data rate system.

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>II</td>
<td>Point to point communication: Detection, Diversity and Channel uncertainty Detection in rayleigh fading channel, time diversity, antenna diversity, frequency diversity, impact of channel uncertainty.</td>
<td>6</td>
</tr>
<tr>
<td>III</td>
<td>Radio Wave Propagation: Free space propagation model- basic propagation mechanisms –reflection- ground reflection model diffraction-scattering-practical link budget design-outdoor and indoor</td>
<td>7</td>
</tr>
</tbody>
</table>
### Capacity of Wireless Channels:
- Introduction
- Capacity in AWGN
- Capacity of Flat-Fading Channels
- Channel and System Model
- Channel Distribution Information (CDI)
- Channel Side Information at Receiver
- Channel Side Information at the Transmitter and Receiver
- Capacity with Receiver Diversity
- Capacity Comparisons
- Capacity of Frequency-Selective Fading Channels
- Time-Invariant Channels
- Time-Varying Channels

### Multiple Antenna Systems:
- Multiple Input Multiple Output (MIMO) Systems
- The Narrowband Multiple Antenna System Model
- Transmit Precoding and Receiver Shaping
- Parallel Decomposition of the MIMO Channel
- MIMO Channel Capacity
- Beamforming
- Space-time codes
- Smart Antennas

### MIMO and multicarrier modulation:
- Narrowband MIMO model
- Parallel decomposition of MIMO channel
- MIMO channel capacity
- MIMO diversity gain – data transmission using multiple carriers
- Multicarrier modulation with overlapping subchannels
- Mitigation of subcarrier fading
- Basic concepts of OFDM

### Text Books

### Reference Books

### List of Experiments (Min 8)
1. Introduction of mobile trainer.
2. Wireless channel modeling and diversity using MATLAB
3. Analysis of broadband wireless channel modeling using MATLAB
4. Analysis of Spread Spectrum communication technique using MATLAB
5. Analysis of MIMO Diversity using MATLAB
6. Study of propagation loss using Okumura – Hata model
7. Free Space Propagation – Path Loss Model
8. Study of Active/passive satellites, uplink/downlink & Transponders
9. Write a code for Rayleigh fading channel.
10. Write a code for Log-distance path loss model.
11. Write a code for PN sequence generation

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**SHIVAJI UNIVERSITY, KOLHAPUR**  
M.E.(Electronics and Telecommunication Engineering) Part- I  
w.e.f July 2015  

**4. Subject: Random Processes**

<table>
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<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
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</thead>
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<tr>
<td>Lectures: 03hrs / week</td>
<td>Theory: 100Marks</td>
</tr>
<tr>
<td>Practical: 02hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. Develop the logical concepts of probability theory
2. Understand basic concepts of Random variables & Random Processes
3. Study concept of Markov Chain and Queuing Theory

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Solve Probability Problems
2. Classify Random Variables
3. Apply statistical measures in Practical problems
4. Apply Markov Chain & Queuing Theory to solve Problems
<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td>Probability Theory: The concept of Probability; the axioms of Probability; sample space and events; Conditional probability and Baye’s theorem, Independence of events, Bernoulli trails.</td>
<td>6</td>
</tr>
<tr>
<td>II</td>
<td>Random variables: Introduction to Random Variables, Discrete Random Variable, Continuous Random Variable, Expectation of Random Variable, Moments of Random Variable (mean, mode variance, skewness, Kurtosis)</td>
<td>6</td>
</tr>
<tr>
<td>III</td>
<td>Multiple Random Variables: cumulative distribution function and probability density function of single and multiple Random Variables, statistical properties, Jointly distributed Gaussian random variables, Conditional probability density, properties of sum of random variables, Central limit theorem, Estimate of population means, Expected value and variance and covariance.</td>
<td>6</td>
</tr>
<tr>
<td>V</td>
<td>Markov Chains: Chapman Kolmogorov equation, Classification of states, Limiting probabilities, Stability of Markov system, Reducible chains, Markov chains with continuous state space.</td>
<td>6</td>
</tr>
<tr>
<td>VI</td>
<td>Queuing Theory: Elements of Queuing System Little's Formula, M/M/1 Queue, Multi server system</td>
<td>6</td>
</tr>
</tbody>
</table>

**Text Books**

1. Introduction to probability Models, (Third edition) - Sheldon M. Ross.
2. Random Signal Processing, Prof. G.V. Kumbhojkar, C. Jamanadas & Company

**Reference Books**

1. Probability and Random Processes for Electrical Engg. - Alberto Lean-Garcia (Pearson Education.)
List of Experiments (Minimum 8)
1. Bernoulli Trials (Binomial Distribution)
2. Study of Rayleigh Probability Density Function
3. Probability density function of Gaussian distributed random variable
4. Effect of changing the Mean and Standard deviation in Gaussian PDF
5. Study of autocorrelation
6. Study of crosscorrelation
7. Study of markov chain
8. Power spectral density (psd)
9. Calculation of various statistical parameters for random signal

SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- I
w.e.f July 2015

5. Subject: Mobile Computing (Elective-1 )

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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<tbody>
<tr>
<td>Lectures: 03 hrs / week</td>
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</tr>
<tr>
<td>Tutorial: 01 hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

Course Objectives: The course aims to:

1. Introduce an advanced element of learning in the field of wireless communication.
2. Introduce students to the concepts of wireless devices and mobile computing.
3. Introduce mobile computing principles that support connectivity to cellular networks, mobile internet.
4. Understand the principles of security in mobile networks and their applications.

Course Outcomes: Upon successful completion of this course, the student will be able to:

1. Understand the characteristics and limitations of mobile hardware devices including their user-interface modalities with wireless communication.
2. Develop applications that are mobile-device specific and demonstrate current practice in mobile computing contexts.
3. Design and develop context-aware solutions for mobile devices
4. Aware of professional and ethical issues, in particular those relating to security and privacy of user data and user behavior.
### Unit 1: Introduction to mobile computing
- Introduction, added dimensions of Mobile Computing, condition of mobile users, Architecture of Mobile software Applications
- Lectures: 7

### Unit 2: Introduction to mobile Development Frameworks and Tools
- Fully centralized frameworks and tools, N-tier client server frameworks and tools, JAVA, BREW
- Lectures: 6

### Unit 3: Mobile Internet
- WAP 1.1 Architecture, wireless application environment, WAP 2.0 Architecture, i-mode
- Lectures: 5

### Unit 4: VUI and Applications
- Introduction, Qualities of speech, voice transcription, voice recognition, text to speech technologies, converting written language to spoken language
- Lectures: 6

### Unit 5: Mobility and Location based services
- Introduction, data acquisition of location information, GIS, location information modeling, location based services applied, utilizing location based services with mobile application, representing location with UML, security and privacy of location information, localization
- Lectures: 6

### Unit 6: Architecture, Design, and Technology Selection for Mobile Applications
- Introduction, Practical Concerns with Architectures, Architectural Patterns for Mobile Applications. Security issues in mobile computing, Authentication, encryption, Characteristics of SIM, Security application development for mobile OS
- Lectures: 6

#### Text Books

#### Reference Books
2. Jochen Schiller, Mobile Communication, Pearson Education Asia

**Minimum 8 tutorials based on above syllabus**
SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- I
w.e.f July 2015

5.Subject: DIGITAL DATA COMPRESSION (Elective-1)

<table>
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<tr>
<td>Tutorial: 01hrs / week</td>
<td>TW : 25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. To provide students with contemporary knowledge in Data Compression and Coding.
2. To equip students with skills to analyze and evaluate different Data Compression and Coding methods.

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Explain the evolution and fundamental concepts of Data Compression and Coding techniques.
2. Analyze the operation of a range of commonly used Coding and Compression techniques.
3. Identify the basic software and hardware tools used for data compression.
4. Identify what new trends and what new possibilities of data compression are available.

<table>
<thead>
<tr>
<th>Unit No</th>
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<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td><strong>Introduction</strong></td>
<td>4</td>
</tr>
<tr>
<td></td>
<td>Definitions, Historical background, Applications, Taxonomy, Intuitive Compression, Run-Length Encoding, RLE Text Compression, RLE Image Compression, MoveToFront Coding, Scalar Quantization</td>
<td></td>
</tr>
<tr>
<td>II</td>
<td><strong>Statistical Methods</strong></td>
<td>7</td>
</tr>
<tr>
<td>III</td>
<td><strong>Dictionary Methods</strong></td>
<td>7</td>
</tr>
<tr>
<td></td>
<td>String Compression, Simple Dictionary Compression, LZ77 (Sliding Window), LZSS, Repetition Times, QIC-122, LZX, File Differencing: VCDIFF, LZ78, LZFG, LZRW1, LZRW 4, LZW, LZMW, LZAP, LZY, LZP</td>
<td></td>
</tr>
</tbody>
</table>
### IV

**Image Compression Approaches to Image Compression;**
Image Transforms, Orthogonal Transforms. The Discrete Cosine Transform JPEG, JPEG-LS. Progressive Image Compression, JBIG, JBIG2, Vector Quantization, Adaptive Vector Quantization, Block Matching, Block Truncation Coding, Context-Based Methods, Wavelet Methods

**Video Compression**
Analog Video, Composite and Components Video, Digital Video, Video Compression, MPEG, MPEG-4, H.261

**Audio Compression**

### V

- **Text Books**
  1. The Data Compression- Mark Nelson, Jean-Ioup Gailly, 2nd edition, (M&T pub.) press ltd.)
  2. Data Compression: The complete Reference-David Saloman, D., 3rd ed, (Springer publication.)
  3. Introduction to Data Compression-Khalid Sayood, 2nd ed. (Academic press ltd.)

- **Reference Books**
  1. Introduction to Information Theory and Data Compression- Darrel Hankerson, 2nd ed, (Chapman and Hall/CRC publications.)
  3. Compression Algorithms for Real Programmers- Peter Wayner (Academic press ltd.)

**Minimum 8 tutorials based on abovesyllabus**

SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part-I
w.e.f July 2015

5.Subject: Internet traffic engineering (Elective 1)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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<tbody>
<tr>
<td>Lectures: 03 hrs / week</td>
<td>Theory: 100 Marks</td>
</tr>
<tr>
<td>Tutorial: 01 hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. Determine link weights for IP traffic engineering for an interior gateway protocol (IGP) such as OSPF or IS-IS.
2. To discuss traffic engineering for IP intra-domain networks.
3. Develop the platform for understanding the basics of routers and types of routers, and as the background material to understand more details about a router’s critical functions, such as address lookup and packet class classification.
Make student to understand algorithms for efficient packet classification to offer differentiated services based agreements

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

<table>
<thead>
<tr>
<th>No.</th>
<th>Course Outcomes</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Estimate traffic in the network, as well as what performance measures might be of interest in IP networks</td>
</tr>
<tr>
<td>2</td>
<td>Evaluate various IP router architectures and highlight their advantages and disadvantages</td>
</tr>
<tr>
<td>3</td>
<td>Evaluate performance requirements of a packet classification algorithm in terms of number of memory accesses and the amount of storage requirement</td>
</tr>
<tr>
<td>4</td>
<td>Solve set of routing and traffic engineering problems in which MPLS can be used by giving due consideration to path management, traffic assignment, network information dissemination, and network management.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td><strong>IP traffic engineering:</strong> Evolution of Traffic engineering in internet domain, Taxonomy and recommendation for internet traffic engineering, Performance Measures and characteristics, applications view and traffic models, Architectural framework, link weight determination, Duality of the MCNF Problem</td>
<td>6</td>
</tr>
<tr>
<td>III</td>
<td><strong>Analysis of IP address lookup Algorithms:</strong> Network Bottleneck, Network Algorithmics, Strawman solutions, Thinking Algorithmically, Refining the Algorithm, Cleaning up, Characteristics of Network Algorithms. IP Address Lookup Algorithms : Impact, Address Aggregation, Longest Prefix Matching, Naïve Algorithms, Binary , Multibit and Compressing Multibit Tries,</td>
<td>6</td>
</tr>
<tr>
<td>IV</td>
<td><strong>IP Packet Filtering and Classification</strong> Search by Length Algorithms, Search by Value Approaches, Hardware Algorithms, Comparing Different Approaches IP Packet Filtering and Classification : Classification, Classification Algorithms, Naïve Solutions, Two-Dimensional Solutions, Approaches for d Dimensions,</td>
<td>6</td>
</tr>
<tr>
<td>---</td>
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<td></td>
</tr>
</tbody>
</table>

**Text Books**

1. Network Routing: Algorithms, Protocols, and Architectures Deepankar Medhi and Karthikeyan Ramasamy, Morgan Kaufmann

2. The competitive internet service provider, network architecture, interconnection, traffic engineering and network design, Oliver Heckmann John Wiley & Sons Ltd,

**Reference Books**

1. Network Algorithmic: An Interdisciplinary Approach to Designing Fast Networked Devices George Varghese (Morgan Kaufmann Series in Networking)


3. Traffic Engineering with MPLS By Eric Osborne, Ajay Simha Publisher: Cisco Press

**Minimum 8 tutorials based on above syllabus**
5. Subject: Advanced Antenna theory (Elective 1)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lectures: 03 hrs / week</td>
<td>Theory: 100 Marks</td>
</tr>
<tr>
<td>Practical: 01 hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. Develop the logical concepts of antenna theory
2. Understand basic concepts of antennas and various antenna parameters
3. Able to design of various types of antennas
4. Develop skills necessary to solve practical problems on antenna designs

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Design different types of antennas
2. Measure different parameters of the design antenna
3. Understand the concept of smart antenna

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td>ANTENNA ARRAYS</td>
<td>7</td>
</tr>
<tr>
<td></td>
<td>N element linear arrays – uniform amplitude and spacing - Directivity, Design Procedure, N-Element Linear Array: Three-Dimensional Characteristics, Rectangular-to-Polar Graphical Solution, N-Element Linear Array: Uniform Spacing, Nonuniform Amplitude, Superdirectivity, Planar Array, Design Considerations, Circular Array</td>
<td></td>
</tr>
<tr>
<td>II</td>
<td>ANTENNA SYNTHESIS</td>
<td>7</td>
</tr>
</tbody>
</table>
### Text Books


2. Antenna Theory- C. A. Balanis- Wiley and sons

### Reference Books

- Antenna Theory and Design – Stutzman and Warren L., John Wiley & Sons
- Antenna Arrays: A Computational Approach’ By Randy L. Haupt, John Wiley and Sons

**Minimum 8 tutorials based on above syllabus**
SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- II
w.e.f July 2015

1. Subject: Advanced Microwave Circuit Design

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lectures : 03 hrs / week</td>
<td>Theory : 100 Marks</td>
</tr>
<tr>
<td>Practical: 02 hrs / week</td>
<td>TW : 25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. Analyze transmission-line circuits at RF and microwave frequencies.
4. Design RF Filters, Amplifiers, Oscillators & mixers.
5. Study of Microwave Integrated Circuits.

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Understand RF and Microwave circuit analysis techniques.
2. Understand transmission line circuits and Microstrip lines.
4. Design microwave small signal and power amplifiers, oscillators & mixers.
5. Understand Microwave Integrated Circuits & processing techniques.

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td>Introduction:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Importance of Radio frequency design, RF behaviour of passive components, Chip components and circuit board consideration. Transmission line Analysis, Strip line &amp; microstrip line, Smith Chart.</td>
<td>6</td>
</tr>
<tr>
<td>II</td>
<td>Microwave Network Analysis:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Interconnecting Networks, Network properties &amp; applications, Scattering parameters, Impedence matching using discrete components, microstrip line matching networks, biasing networks.</td>
<td>6</td>
</tr>
</tbody>
</table>
### III RF Filter Design:
- Basic resonator & Filter configurations, special filter realisations, Filter implementations, Coupled filters

### IV RF Transistor Amplifier Design:
- Characteristics of amplifiers, Amplifier power relations, Stability considerations, Constant gain, Noise figure circles, Constant VSWR circles, Broadband Highpower & Multistage Amplifiers.

### V Oscillator and Mixture Design:
- Basic Oscillator Model, High frequency Oscillator configuration, Basic characteristics of Mixers & mixer design.

### VI Microwave Integrated Circuits:
- Materials & basic fabrications technologies of Hybrid ICs & monolithic ICs, Examples of IC Fabrication flow, MICs- amplifiers, Oscillators, Mixers, Frequency dividers, Digital modulators, Switches, Phase shifters, Multipliers & Upconverters.

#### Text Books
2. D.M.Pozar, “Microwave Engineering.”, John Wiley & sons

#### Reference Books

#### List of Experiments:(Minimum 8)
1. To determine Transmission line parameters.
2. Impedance and frequency measurement using MW Test bench
3. Waveguide parameters measurement using MW Test bench
4. Scattering parameters of Circulator using MW Test bench
5. Scattering parameters of Magic Tee using MW Test bench
6. To measure the magnitude and phase of reflection and transmission coefficients (S parameters) of one port network using Vector Network Analyzer
7. To measure the magnitude and phase of reflection and transmission coefficients (S parameters) of Two port network using Vector Network Analyzer
8. Design of matching networks
9. To study & test performance of Microwave amplifiers.
10. To study & design of RF filters
(Note: Experiments are not limited to above list)

SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- II
w.e.f July 2015

2. Subject: Adhoc and Wireless Sensor Networks

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Lectures : 03hrs / week</td>
<td>Theory : 100 Marks</td>
</tr>
<tr>
<td>Practical: 02hrs / week</td>
<td>TW :25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. To study different issues and challenges in Adhoc wireless network and deriving application specific solutions.
2. To study technologies and challenges in Cognitive Radio Network
3. To study architecture and different protocols in Wireless Sensor Networks
4. To study practical perspective on Wireless Sensor Networks

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Students will able to develop desired protocols as per the requirement of applications.
2. Students will able to provide solution over spectrum access.
3. Students will able to design a suitable protocol as per the requirement
4. Students will able to implement practical knowledge for design and development of Wireless Sensor Network Applications.
<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td><strong>Introduction:</strong> Introduction to adhocnetworks, Challenges Applications of MANETs, IOT and Applications of IOT</td>
<td>4</td>
</tr>
<tr>
<td>II</td>
<td><strong>Communication Protocols in Adhoc Networks:</strong></td>
<td>8</td>
</tr>
<tr>
<td></td>
<td>MAC Protocols: design issues, goals and classification. Contention based protocols-with reservation, scheduling algorithms, protocols using directional antennas. IEEE standards: 802.11a, 802.11b, 802.11g, 802.15. Routing Protocols: Topology based, position based and power aware routing</td>
<td></td>
</tr>
<tr>
<td>III</td>
<td><strong>Cognitive Radio and Networks:</strong> Introduction, Spectrum access Model, CR technologies and challenges IEEE 802.22 standard: TV bands and Pus. applications ref. architecture</td>
<td>6</td>
</tr>
<tr>
<td>VI</td>
<td><strong>A Practical Perspective on Wireless Sensor Networks:</strong> Introduction, WSN Applications, Classification of WSNs, Characteristics, Technical Challenges, and Design Directions, Technical Approaches.</td>
<td>6</td>
</tr>
</tbody>
</table>

**Text Books**

Reference Books


List of Experiments( Minimum 8)

1. Study of wireless network setup and analysis.
3. Measurement of network throughput
4. Measurement of Packet delivery ratio
5. Measurement of bandwidth overhead
6. Measurement of packet latency
8. Design and simulate DSDV (Destination Sequenced Distance Vector) protocol in ns2.
9. Design and simulate AODV (Ad hoc On Demand Distance Vector Routing) Protocol in ns2

SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- II
w.e.f July 2015
3.Subject: Computer Vision

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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</tr>
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<tbody>
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<td>Lectures : 03 hrs / week</td>
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</tr>
<tr>
<td>Practical: 02 hrs / week</td>
<td>TW : 25 Marks</td>
</tr>
</tbody>
</table>

Course Objectives: The course aims to:

1. Understand concepts of Image Segmentation
2. Provide logical base for Feature Extraction
3. Study different Classifiers
4. Apply concept of ANN
Course Outcomes: Upon successful completion of this course, the student will be able to:

1. Segment desired objects from Image
2. Extract the features from objects/Image
3. Apply classifier techniques

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
</table>
| I       | Introduction:  
Introduction to Computer Vision System and applications  
Fundamentals of digital images: representation, Pixel geometry  
Image Enhancement: gray level transformation , histogram processing | 5 |
| II      | Image Segmentation:  
Fundamentals; Point, line and Edge detection; Thresholding; Region based segmentation | 7 |
| III     | Representation and Description:  
Representation; BoundaryDescriptors; RegionalDescriptors; Use of principle components for description; Relational Descriptors | 9 |
| IV      | Pattern Recognition:  
Overview of pattern recognition; Patterns and pattern Classes | 2 |
| V       | Classifications:  
Decision making; Statistical Decision making; Nearest neighbour classifier;  
Clustering : Basic concepts, Classical schemes; Model based methods, Fuzzy clustering | 7 |
| VI      | Artificial neural networks:  
Human Recognition system; Artificial neural networks; Different models of Artificial neural networks; Perceptron and learning; | 6 |

Text Books
1. Digital Image processing and Pattern Recognition by Malay K. Pakhira (PHI)
2. Digital Image processing by Rafael c. Gonzalez and Richard E. Woods (Pearson Education)

Reference Books
2. Digital image processing and analysis by B. Chanda, D. Dutta Mujumdar (PHI)
3. Image processing, analysis and machine vision by Milan sonka, V. Hlavac, R. Boyle (Thomson learning)
List of Experiments (Minimum 8)

1. Gray level Transformation
2. Histogram equalization
3. Smoothing linear filters
4. Edge Detection techniques
5. Image segmentation using Thresholding
7. Boundary / Regional descriptor
8. Statistical Decision making
9. Nearest neighbor classifier
10. ANN Perceptron Model

SHIVAJI UNIVERSITY, KOLHAPUR
M.E. (Electronics and Telecommunication Engineering) Part- II
w.e.f July 2015

4. Subject: Cryptography & Network security (Elective-II)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Lectures: 03 hrs / week</td>
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</tr>
<tr>
<td>Tutorial: 01 hrs / week</td>
<td>TW : 25 Marks</td>
</tr>
</tbody>
</table>

Course Objectives: The course aims to:

1. Understand Block Cipher and DES principles
2. Understand Symmetric Encryption Methods
3. Identify network security threat
4. Understand Key Resources and management resources

Course Outcomes: Upon successful completion of this course, the student will be able to:

1. Implement Cryptography methods on Network Security concepts and Application
2. Implement Symmetric methods
3. Implement Message authentication and Hash Functions
4. Identify the attacks and methods of web security
<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>II</td>
<td>Block Ciphers and the Data Encryption Standard: Simplified DES, Block Cipher Principles, The Data Encryption Standard, The Strength of DES, Differential Linear Cryptanalysis, Block Cipher Design Principles, Block Cipher Modes of Operation.</td>
<td>5</td>
</tr>
<tr>
<td>IV</td>
<td>Public Key Cryptography and RSA: Principles of Public Key cryptosystems, The RSA Algorithm, Key Management, other Public Key Cryptosystems key Management, Diffie-Hellman Key exchange</td>
<td>5</td>
</tr>
</tbody>
</table>

**Text Books**


**Reference Books**


**Minimum 8 tutorials based on above syllabus**
SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- I
w.e.f July 2015
4.Subject: Multirate systems(Elective-II)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lectures: 03hrs / week</td>
<td>Theory: 100 Marks</td>
</tr>
<tr>
<td>Tutorial: 01hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. To provide basic concepts of Multirate systems
2. To give inputs regarding details of Multirate filter banks and their types.
3. To provide concepts of Multidimensional Multirate Systems
4. To provide information of different applications of Multirate Systems

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Understand the basic multi-rate operations
2. Apply the concept of Multirate filter banks.
3. Implement the design of Multirate filter banks
4. Understand the role of Multirate systems in different applications.

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td><strong>Fundamentals of Multi-rate Systems:</strong> Basic multi-rate operations, interconnection of building blocks, polyphase representation, multistage implementation.</td>
<td>6</td>
</tr>
<tr>
<td>II</td>
<td><strong>Multirate Filter Banks:</strong> Maximally decimated filter banks: Errors created in the QMF bank, alias-free QMF system, power symmetric QMF banks, M-channel filter banks, poly-phase representation, perfect reconstruction systems, alias-free filter banks, tree structured filter banks, transmultiplexers.</td>
<td>6</td>
</tr>
<tr>
<td>III</td>
<td><strong>Para-unitary Perfect Reconstruction Filter Banks:</strong> Lossless transfer matrices, filter bank properties induced by paraunitariness, two channel Para-unitary lattices, M-channel FIR Para-unitary QMF banks, transform coding.</td>
<td>6</td>
</tr>
</tbody>
</table>
# Linear Phase Perfect Reconstruction QMF Banks

Necessary conditions, lattice structures for linear phase FIR PR QMF banks, formal synthesis of linear phase FIR PR QMF lattice. Cosine Modulated Filter Banks: Pseudo-QMF bank and its design, efficient polyphase structures, properties of cosine matrices, cosine modulated perfect reconstruction systems

**V**

# Multidimensional Multirate Systems

Introduction, Multidimensional signals and their sampling, minimum sampling density, Multirate fundamentals, Alias free decimation. Cascade connections, Multirate filter design. Special filters and filter banks.

**VI**

# Applications

FSK Modems, OMC data transmission, DAB and ADSL, Asynchronous conversion of sampling rates, Speech and audio coding, Image and video coding, Simulation of room acoustics using Wavelets, Multirate techniques with sensors

### Text Books


### Reference Books


**Minimum 8 tutorials based on abovesyllabus**

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**SHIVAJI UNIVERSITY, KOLHAPUR**

M.E.(Electronics and Telecommunication Engineering) Part-II

w.e.f July 2015

4.Subject: SDR & Cognitive Radio Technology(Elective-II)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
</table>
### Course Objectives:
The course aims to:

1. Understand concept of SDR and Cognitive radios.
2. Know COBRA, SCA, JTRS.
3. Understand concept of smart antenna.

### Course Outcomes:
Upon successful completion of this course, the student will be able to:

1. Enable the student to understand the evolving paradigm of cognitive radio communication and the enabling technologies for its implementation.
2. Enable the student to understand the essential functionalities and requirements in designing software defined radios and their usage for cognitive communication.
3. Expose the student to the evolving next generation wireless networks and their associated challenge.

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>I</strong></td>
<td>SDR concepts &amp; history, Benefits of SDR, SDR Forum, Ideal SDR architecture, SDR Based End-to-End Communication, Worldwide frequency band plans, Aim and requirements of the SCA.</td>
</tr>
<tr>
<td><strong>III</strong></td>
<td>Radio Frequency design, Baseband Signal Processing, Radios with intelligence, Smart antennas, Adaptive techniques, Phased array antennas, Applying SDR principles to antenna systems, Smart antenna architectures.</td>
</tr>
<tr>
<td><strong>IV</strong></td>
<td>Low Cost SDR Platform, Requirements and system architecture, Convergence between military and commercial systems, The Future For Software Defined Radio.</td>
</tr>
</tbody>
</table>
VI

Radio Frequency design, Baseband Signal Processing, Radios with intelligence, Smart antennas, Adaptive techniques, Phased array antennas, Applying Cognitive radio principles to antenna systems, Smart antenna architectures.

Text Books

2. Reed: Software Radio, Pearson

Reference Books

2. Tafazolli (Ed.): Technologies for the Wireless Future, Wiley 2005

**Minimum 8 tutorials based on abovesyllabus

SHIVAJI UNIVERSITY, KOLHAPUR
M.E.(Electronics and Telecommunication Engineering) Part- II
w.e.f July 2015

4.Subject: DSP Architecture & Algorithm(Elective-II)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Lectures: 3 hrs / week</td>
<td>Theory: 100 Marks</td>
</tr>
<tr>
<td>Tutorial: 1 hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

Course Objectives:
The course aims to:
1. Understand DSP Processor.
Understand Architecture and Programming of **TMS32054XX**.

Understand Architecture of **TMS320C6XXX**.

Understand DSP Algorithms.

**Course Outcomes:**
Upon successful completion of this course, the student will be able to:

1. Differentiate various DSP Processors.
2. Implement Onchip Peripherals of DSP Processors.
3. Implement DSP algorithms.
4. Design application programming using DSP processors.

<table>
<thead>
<tr>
<th>Unit No</th>
<th>UNIT NAME</th>
<th>No. of Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td>Introduction to Programmable DSPs</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td>Comparison of GP Processors and DSP processor Architecture, Multiplier and MAC, Modified Bus structures and Memory Access schemes, Multiple Access Memory, Dual port memory, VLIW Architecture, Pipelining, Special Addressing Modes, On-Chip Peripherals, RISC Vs CISC design</td>
<td>6</td>
</tr>
<tr>
<td>II</td>
<td>Architecture of TMS32054XX</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td>Introduction, Architecture, buses, Memory organization, CPU, ALU, Barrel Shifter, On-chip Peripherals, Address Generation Logic</td>
<td>6</td>
</tr>
<tr>
<td>III</td>
<td>TMS32054XX Assembly Language</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td>Instructions, Programming in Assembly language</td>
<td>6</td>
</tr>
<tr>
<td>IV</td>
<td>Architecture of TMS320C6XXX</td>
<td>8</td>
</tr>
<tr>
<td></td>
<td>Features, Architecture, Memory Interfacing, Addressing Modes, Pipeline operation, Peripheral, C-Programming and DSP Application development like Speech coding Image processing and coding applications</td>
<td>8</td>
</tr>
<tr>
<td>V</td>
<td>Implementations of basic DSP algorithms: An FFT Algorithm for DFT Computation, FIR Filters, IIR Filters, Interpolation Filters, Decimation Filters, PID Controller, Adaptive Filters, 2-D Signal Processing, FPGA Based DSP System Design, with Code Composer Studio.</td>
<td>6</td>
</tr>
<tr>
<td>VI</td>
<td>Application Programming TMS320C54XX/TMS320C6XXX</td>
<td>4</td>
</tr>
<tr>
<td></td>
<td>Application ProgrammesonTMS320C54XX/TMS320C6XXX with Code Composer Studio.</td>
<td>4</td>
</tr>
</tbody>
</table>

**Text Books:**

**Reference Books:**


**Minimum 8 tutorials based on above syllabus**

**SHIVAJI UNIVERSITY, KOLHAPUR**

**M.E.(Electronics and Telecommunication Engineering) Part- II**

w.e.f July 2015

5. **Subject: Advanced Operating System(Elective-III)**

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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<tbody>
<tr>
<td>Lectures: 03 hrs / week</td>
<td>Theory: 100 Marks</td>
</tr>
<tr>
<td>Tutorial: 1hrs / week</td>
<td>TW : 25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. Understand the Concept of hardware interface and OS Interface
2. Understand parallel System along with Multiprocessor
3. Understand IPC patterns
4. Understand the concept of Process along with I/O devices and System

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Implement hardware interface along with addressing and interrupts
2. Implement System calls and OS Interface
3. Implement Parallel System for two process system
4. Implement I/O devices and System on OS

<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
</table>
| I | **Introduction & Hardware Interface**  
Introduction, System Levels, Hardware Resources, Resource Management, Virtual Computers, Requirement of Operating system,CPU, Memory and Addressing, Interrupts, I/O Devices. | 10 |
| II | **Operating System Interface**  
System calls & its example, Information and Meta information, Naming OS objects, Devices as Files, Process Concept, Communication between Process, Unix Style Creation, Standard Input and standard output. | 5 |
| III | **Parallel Systems**  
Parallel Hardware, An OS for Two Processor System, Race condition with a shared process table, Atomic actions,Multiprocessor OS : Grouping Shared variables, using two process tables, threads, Implementation of Mutual Exclusion, varieties of computer models. | 5 |
| IV | **Interprocess Communication (IPC) Patterns**  
Using IPC, Patterns of IPC, Problems when Process complete, Race conditions and atomic actions, IPC pattern: Mutual Exclusion, Signalling, Rendezous, procedure consumer, Client Server, Database acess and update, review of IPC pattern | 5 |
| V | **Process**  
Everday Scheduling, Preemptive Scheduling, Policy Vs Mechanism in scheduling, Scheduling in real operating System, Deadlock, Condition to occur, deal with deadlock, Two phase Locking, starvation, Message passing variation | 5 |
| VI | **I/O Devices & System**  
Device and Controllers, Terminal Devices- Basic Terminal, Communication Devices, Disk devices, Disk Controller,SCSI Interface, tape devices, CD devices.  
I/O System software, Access Strategies- Double Buffering, Unification of files and I/O Devices, Disk device drivers, Disk caching, SCSI Device drivers. | 6 |

**Text Books**

1. Distributed Operating System Concept and design By Pradeep K Sinha PHI
**Minimum 8 tutorials based on abovesyllabus**

**SHIVAJI UNIVERSITY, KOLHAPUR**
M.E.(Electronics and Telecommunication Engineering) Part- II
w.e.f July 2015
5. Subject: Optimization techniques(Elective-III)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lectures: 03hrs / week</td>
<td>Theory: 100 Marks</td>
</tr>
<tr>
<td>Practical: 01hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

**Course Objectives:** The course aims to:

1. Students should understand the concept of Optimization Techniques.
2. Students should understand the concept of linear programming, Nonlinear programming, Geometric programming, Dynamic programming.
3. Students should understand the method for formulation of problem and assignment of models.
4. Students should understand single-dimensional and Multi-dimensional Search Methods.

**Course Outcomes:** Upon successful completion of this course, the student will be able to:

1. Students should be able to apply Optimization Techniques to Engineering Problems.
2. Students should be able to implement Linear/Nonlinear, Dynamic, Geometric programming.
3. Students should be able to apply single-dimensional and Multi-dimensional Search Methods in constrained and Unconstrained problem environments.
I | Introduction: Historical development, Application to Engineering Problems, Statement of Optimization problems, Classification of Optimization, Multivariable optimization with and without constraints,

II | **Linear Programming**: Formulation, Geometry, Graphical solution, standard and matrix form of linear programming problems, Simplex programming and its flow chart, revised simplex algorithm, Two-phase Simplex method, Degeneracy.


IV | **Geometric programming**: Problems with positive coefficients up to one degree of difficulty, Generalized method for the positive and negative coefficients.

Dynamic programming: Discrete and continuous dynamic programming (simple illustrations). Multistage decision problems, computation procedure and case studies.

V | **Assignment Models**: Formulation of problem, Hungarian Method for Assignment Problem, Unbalanced Assignment Problems

VI | **Genetic Algorithms**: Introduction, Representation of design variables, Representation of objective function and constraints, Genetic operators, Application procedure and case studies.

Note: - Term Work based on tutorials using MATLAB Optimization Tool Box

Text Books


2. Optimization concepts & application in Engg. - A. D. Belegundu, Tirupati R. Chandrupatla (Pearson Edn.)
Reference Books


**Any 8 tutorials based on above syllabus**

SHIVAJI UNIVERSITY, KOLHAPUR
M.E. (Electronics and Telecommunication Engineering) Part II
w.e.f July 2015

5. Subject: Industrial Automation & Process (Elective-III)

Control

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Lectures: 03hrs / week</td>
<td>Theory: 100 Marks</td>
</tr>
<tr>
<td>Tutorial: 01hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

Course Objectives: The course aims to:


2. Identify Practical Programmable Logic Controller Applications, Know the History of the PLC, Demonstrate Basic PLC Skills.

3. To study basics fuzzy logic and control for industrial automation.

Course Outcomes: Upon successful completion of this course, the student will be able to:

1. Apply basic knowledge of process control techniques.

2. Develop a PLC program for an automatic control systems.

3. Select the right hardware for a given application.

4. Consider such aspects of the automation system as network communication, human machine interface, safety and protection against interference.
<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>IV</td>
<td>Distributed Control Systems: DCS introduction, functions, advantages and limitations, DCS as an automation tool to support Enterprise Resources Planning, DCS Architecture of different makes, specifications, configuration and programming, functions including database</td>
<td>6</td>
</tr>
<tr>
<td>V</td>
<td>Programmable logic controllers (PLC): Introduction, architecture, definition of discrete state process control, PLC vs PC, PLC vs DCS, relay diagram, ladder diagram, ladder diagram examples, relay sequencers, timers/counters, PLC design, Study of at least one industrial PLC</td>
<td>6</td>
</tr>
<tr>
<td>VI</td>
<td>Automation for following industries – Power, Water and Waste Water Treatment, Food and Beverages, Cement, Pharmaceuticals, Sugar, Automobile and Building Automation.</td>
<td>6</td>
</tr>
</tbody>
</table>
Text Books

1. Donald Eckman – Automatic Process Control, Wiley Eastern Limited

Reference Books

1. Process Control Systems - F.G. Shinskey, TMH
Programmable Logic Controllers: Principles and Applications - Webb & Reis PHI

**Minimum 8 tutorials based on above syllabus

SHIVAJI UNIVERSITY, KOLHAPUR
M.E. (Electronics and Telecommunication Engineering) Part-II
w.e.f July 2015
5. Subject: Nanotechnology (Elective-III)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lectures: 03 hrs / week</td>
<td>Theory: 100 Marks</td>
</tr>
<tr>
<td>Tutorial: 01 hrs / week</td>
<td>TW: 25 Marks</td>
</tr>
</tbody>
</table>

Course Objectives: The course aims to:

1. Introduction of Nanoscience & Nanotechnology
2. Study of Semiconductors nanostructure & Nanoparticle
3. Development of Different sensors, Actuators for particular application
   Applications of Nanotechnology in electronics device manufacturing, Medical, Mechanical Industry,

Course Outcomes: Upon successful completion of this course, the student will be able to:

1. Students can understand field of Nanotechnology
2. Students can understand different material used for nanotechnology
3. Students can understand different nano-sensors, Actuators used for various application
4. Students can understand different applications of Nanotechnology
<table>
<thead>
<tr>
<th>Unit No</th>
<th>Unit Name</th>
<th>No. of Lectures</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td>Introduction to Nano Science and Nano Technology</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td>Introduction to Quantum Mechanics; Schrödinger equation and expectation values, Solutions of the Schrödinger equation for free particle, particle in a box, particle in a finite well, Reflection and transmission by a potential step and by a rectangular barrier.</td>
<td></td>
</tr>
<tr>
<td>II</td>
<td>Semiconductors Nanostructure &amp; Nano-particle</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td>Semiconductor nanoparticles – applications, Optical luminescence and fluorescence from direct band gap semiconductor nanoparticles, surface-trap passivation in core-shell nanoparticles, carrier injection, polymer-nanoparticle, LED and solar cells, electroluminescence, barriers to nanoparticle lasers, doping nanoparticles, Mn-Zn-Se phosphors, light emission from indirect semiconductors, light emission form Si nanodots.</td>
<td></td>
</tr>
<tr>
<td>III</td>
<td>Semiconductor nanoparticles: size–dependant physical properties, Melting point, solidstate phase transformations, excitons, band-gap variations-quantum confinement, effect of strain on band-gap in epitaxial quantum dots. The p-n junction and the bipolar transistor; metal semiconductor and metal-insulator, Semiconductor junctions; field-effect transistors, MOSFETs, CMOS: heterostructures, high-electron-mobility devices, HEMTs, Quantum Hall effect, Introduction to single electron transistors (SETs): quantum dots, single electron effects, Coulomb blockade.</td>
<td>6</td>
</tr>
<tr>
<td>IV</td>
<td>NANOELECTRONIC APPLICATIONS</td>
<td>6</td>
</tr>
<tr>
<td>V</td>
<td>INDUSTRIAL NANOTECHNOLOGY</td>
<td>6</td>
</tr>
</tbody>
</table>
VI

Unit-VI BIOMEDICAL APPLICATIONS

Text Books

1. Quantum Physics – A. Ghatak

2. Quantum Mechanics - Bransden and Joachen

Reference Books

1. Encyclopedia of Nanotechnology- Hari Singh Nalwa

2. Springer Handbook of Nanotechnology - Bharat Bhushan

**Minimum 8 tutorials based on above syllabus

Shivaji University, Kolhapur
Revised Syllabus Structure of Third Year Engineering (ME) (w. e. f. 2015)
Electronics and Telecommunication Engineering Course

Subject Equivalence

<table>
<thead>
<tr>
<th>Serial Number</th>
<th>New Subject</th>
<th>Old Subject</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Advance Embedded System</td>
<td>--</td>
</tr>
<tr>
<td>2</td>
<td>Error control Coding Techniques</td>
<td>Linear Algebra &amp; Error Control Techniques</td>
</tr>
<tr>
<td>3</td>
<td>Advance Wireless Communication</td>
<td>Wireless Communication</td>
</tr>
<tr>
<td>4</td>
<td>Random Process</td>
<td>Random Processes</td>
</tr>
<tr>
<td>5</td>
<td>Advance microwave circuit design</td>
<td>RF &amp; Microwave Circuit Design</td>
</tr>
<tr>
<td>6</td>
<td>Adhoc&amp; wireless sensor networks</td>
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<tr>
<td>7</td>
<td>computer vision</td>
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</tr>
<tr>
<td>8</td>
<td>Mobile Computing</td>
<td>Mobile Computing</td>
</tr>
<tr>
<td>9</td>
<td>Digital Data Compression</td>
<td>Digital Data Compression</td>
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<tr>
<td>10</td>
<td>Internet Traffic engineering</td>
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</tr>
<tr>
<td>11</td>
<td>Advanced Antenna Theory</td>
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</tr>
<tr>
<td>12</td>
<td>Cryptography &amp; Network Security</td>
<td>Cryptography &amp; Network Security</td>
</tr>
<tr>
<td>13</td>
<td>Multi rate system</td>
<td>Multi Rate Systems &amp; Filter Banks</td>
</tr>
<tr>
<td>14</td>
<td>SDR &amp; Cognitive Radio Technology</td>
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<tr>
<td>15</td>
<td>DSP architecture &amp; algorithm</td>
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</tr>
<tr>
<td>16</td>
<td>Advanced Operating Systems</td>
<td>----</td>
</tr>
<tr>
<td>17</td>
<td>Optimization techniques</td>
<td>Optimization Technique</td>
</tr>
<tr>
<td>18</td>
<td>Industry automation &amp; process control</td>
<td>----</td>
</tr>
<tr>
<td>19</td>
<td>Nanotechnology</td>
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</tbody>
</table>